



## **Teklatch Reaches Milestone in its Focus on ASIC Design Efficiency**

### **Design efficiency and predictability will drive ROI for modern IC business**

*Copenhagen, Denmark, May 28th 2010* - Teklatch, an industry pioneer in EDA solutions for optimizing digital ASIC power integrity, today announced reaching a milestone in its focus on usability and automation. This focus is part of Teklatch's strategy to push its Dynamic Power Shaping™ technology into the mainstream, helping top tier semiconductor companies improve physical-level convergence across a broad range of design groups and business units, in order to leverage a hidden profit margin. The effort is backed by a new release of FloorDirector®, the world leading EDA solution for optimizing dynamic peak current at an early stage in the backend process, reducing dynamic IR drop, EMI and substrate noise.

“While the core FloorDirector technology has been delivering consistently good results in top tier customer designs, our focus with this release of FloorDirector has been on hands-on experience and on improving ease-of-use. The value to our clients in reducing design space exploration cycle time and providing smooth flow integration is recognized across design groups in all major markets”, said Dr. Tobias Bjerregaard, CEO of Teklatch.

“Our effort is backed by close behind-the-scene collaboration with major EDA tool vendors, for the benefit of common customers.”

“The industry needs more than good technical results. Modern business sense in the semiconductor industry is about design efficiency and predictability. Teklatch's value contribution is focused on three aspects; 1) First Class Results, 2) Ease-of-Use, and 3) Seamless Flow Integration,” Dr. Bjerregaard said, and added “Prioritizing across these three focal points, Teklatch is contributing to the semiconductor industry’s dedication to improve profitability, and we are proud of our contributions. This latest FloorDirector release is an important milestone in our efforts.”

Teklatch will be exhibiting at this year's Design Automation Conference (DAC) in Anaheim, California, June 13-18. At a DAC User Track session, researchers from STMicroelectronics will be presenting a Power Shaping Methodology for Supply Noise and EMI Reduction, which is based on FloorDirector's Dynamic Power Shaping capabilities, and provide extensive validation data for the effectiveness of the technology.

**DAC User Track:** Power Delivery from Package to Chip (STMicroelectronics / Teklatch), Thursday, June 17, 2010 | 9:00AM — 11:00AM | 208AB.